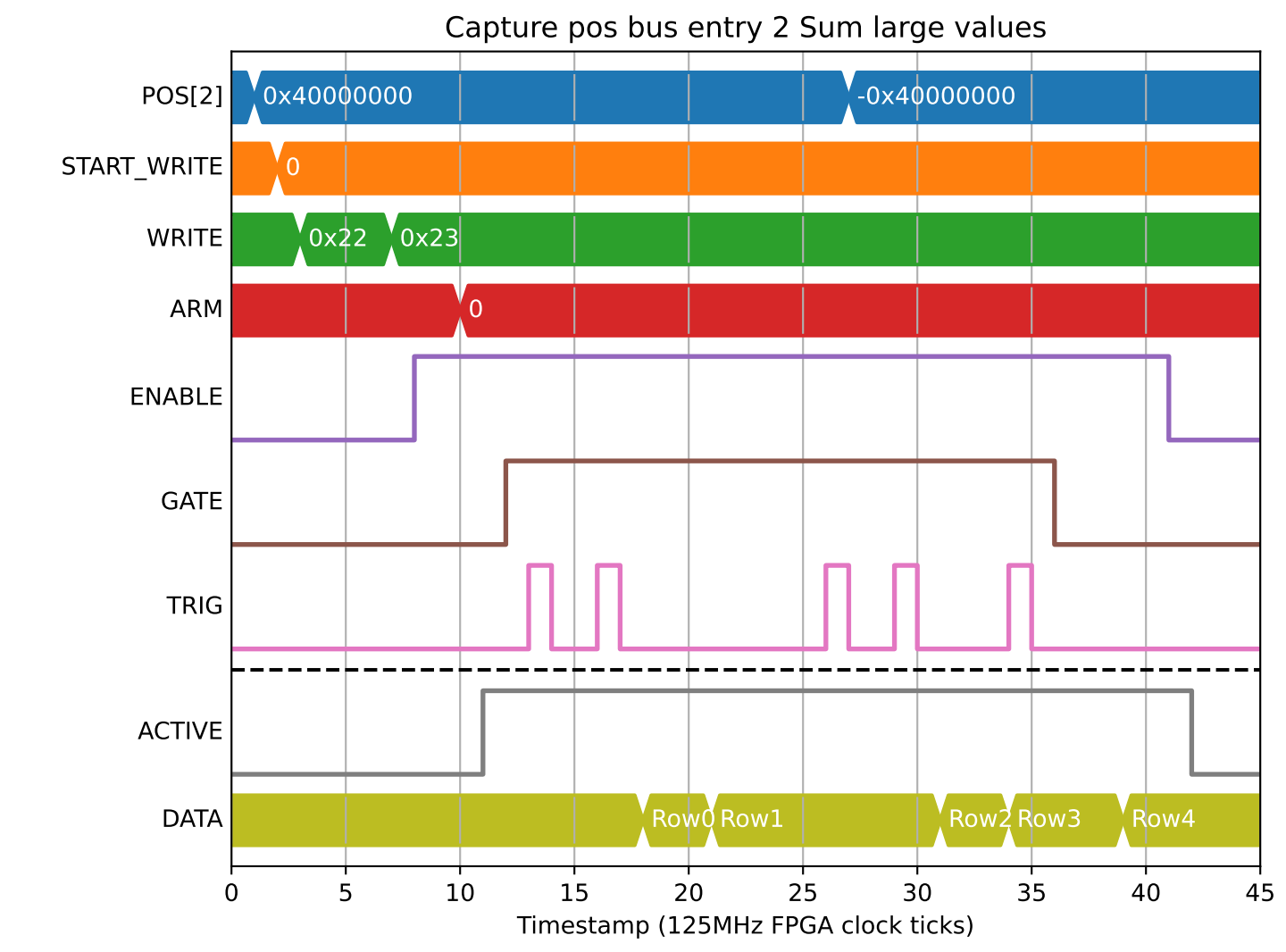
Capture pos bus entry 2 Sum large values
POS[2]
0x40000000
-0x40000000
START_WRITE
0
WRITE
0x22
0x23
ARM
0
ENABLE
GATE
TRIG
ACTIVE
DATA
Row0
Row1
Row2
Row3
Row4
0
5
10
15
20
25
30
35
40
45
Timestamp (125MHz FPGA clock ticks)